

U.S. UTILITY Patent Application

DC	ORI.P.E.	PATENT DATE
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APPLICATION NO. 09/459305	CONT/PRIOR F	CLASS 257	SUBCLASS 314	ART UNIT 2811	EXAMINER J. TIRAW
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APPLICANTS
TOMOYUKI FORUHATA

TITLE
SEMICONDUCTOR DEVICES, METHODS OF MANUFACTURING SEMICONDUCTOR DEVICES, CIRCUIT SUBSTRATES AND ELECTRONIC DEVICES

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

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